FIGURE 1B (Prior Art)

Legend:
- Pointer processing
- → Multiplexing
- → Mapping
- ⇢ Aligning

FIGURE 1A (Prior Art)

FIGURE 2

FIGURE 3

FIGURE 4

FIGURE 5

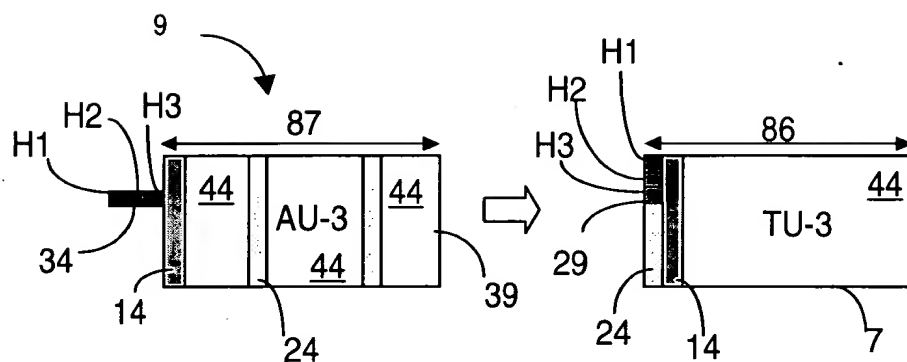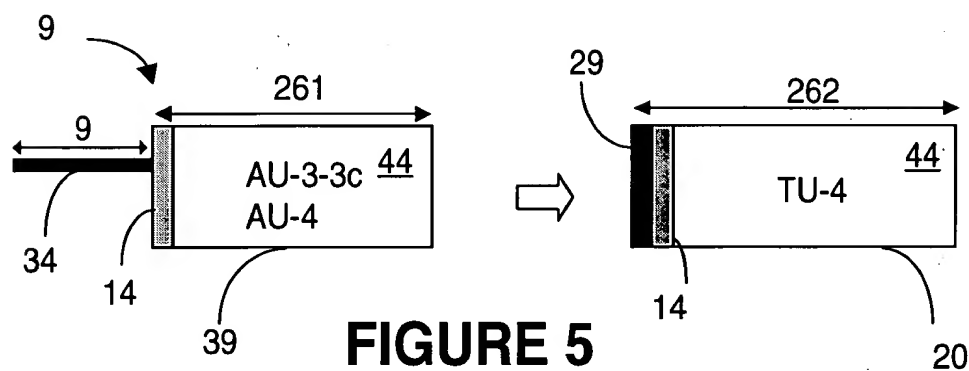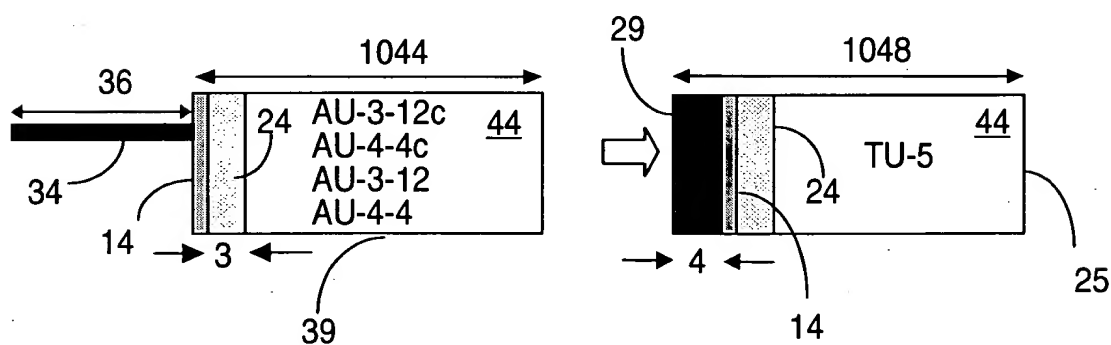**FIGURE 6**
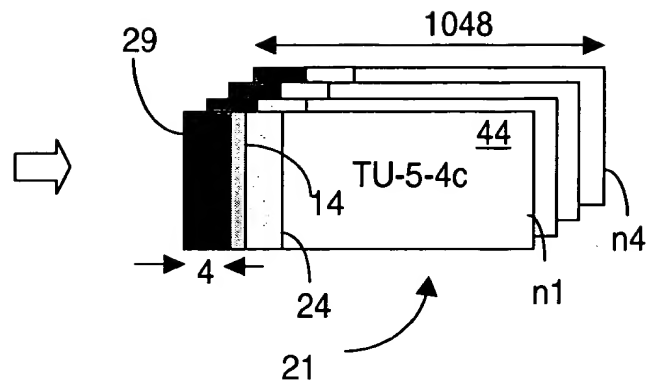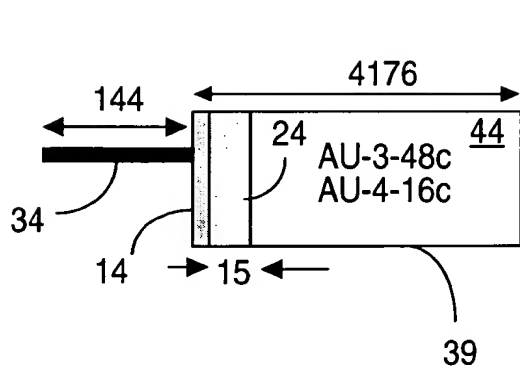
FIGURE 7
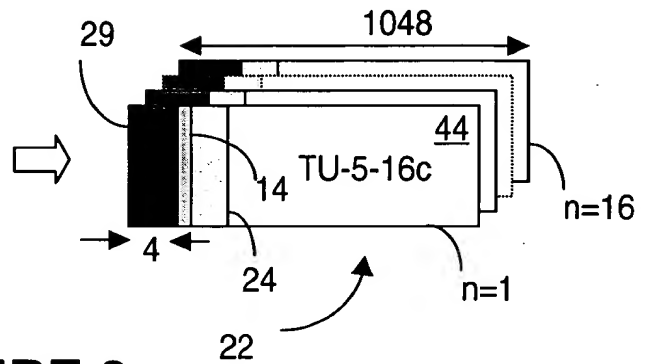
FIGURE 8
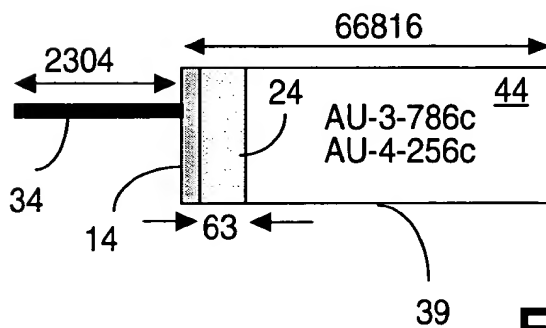
FIGURE 9
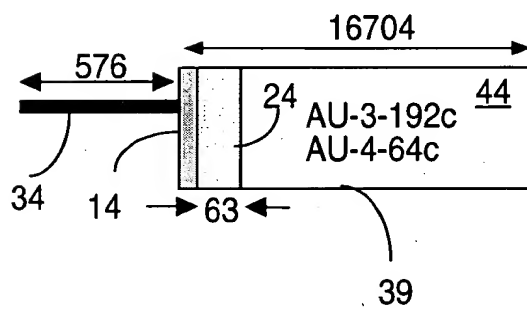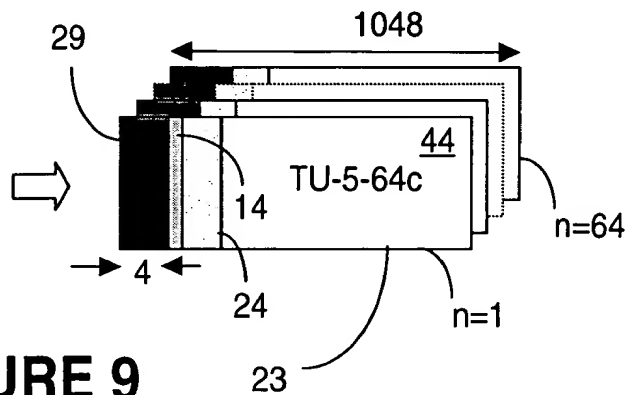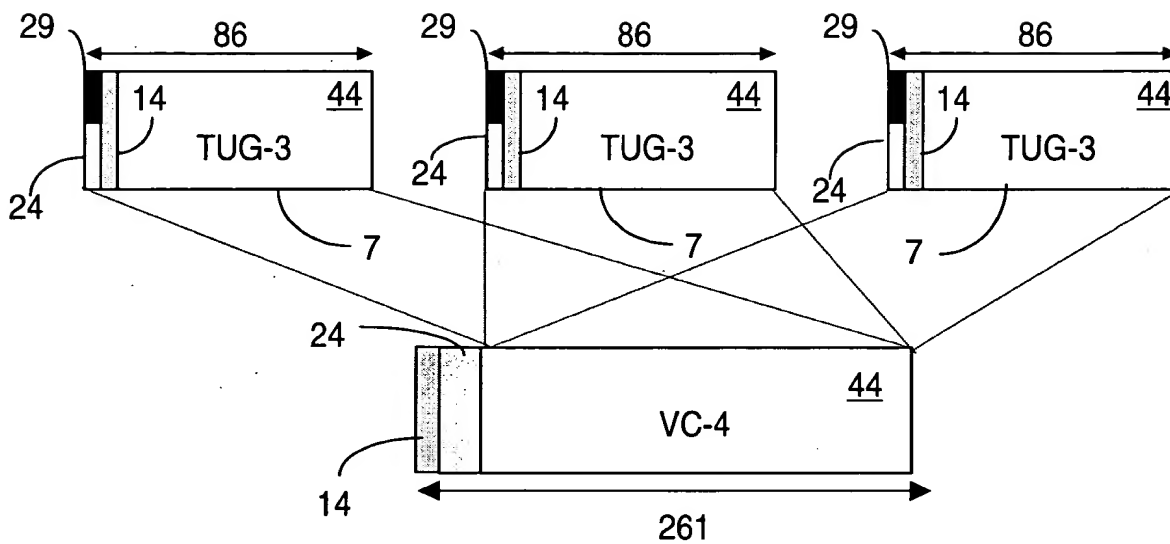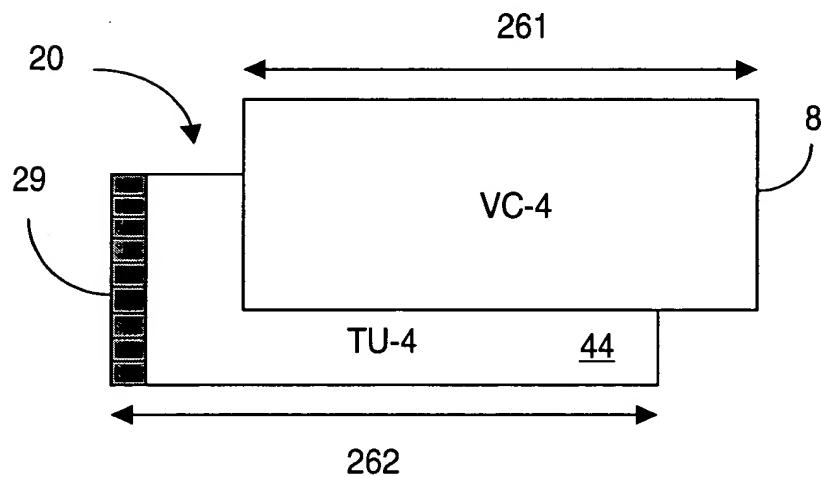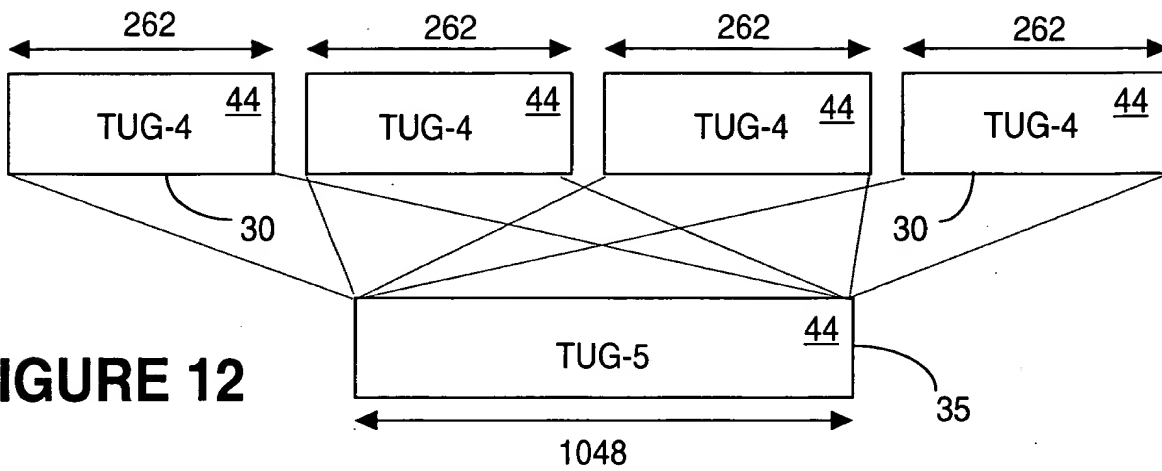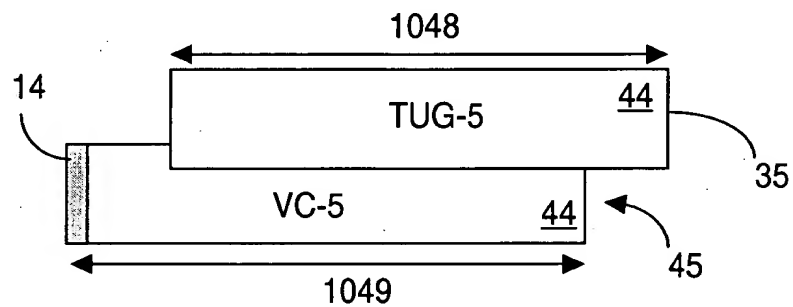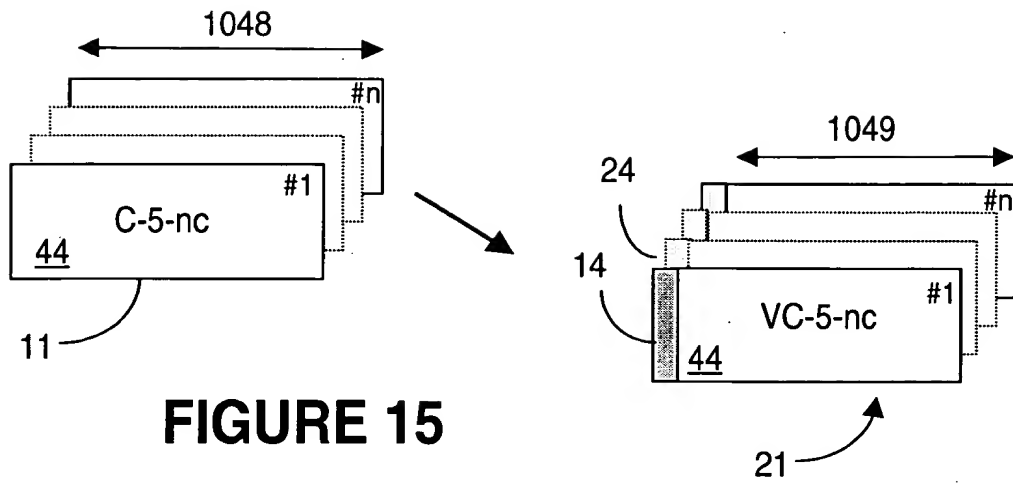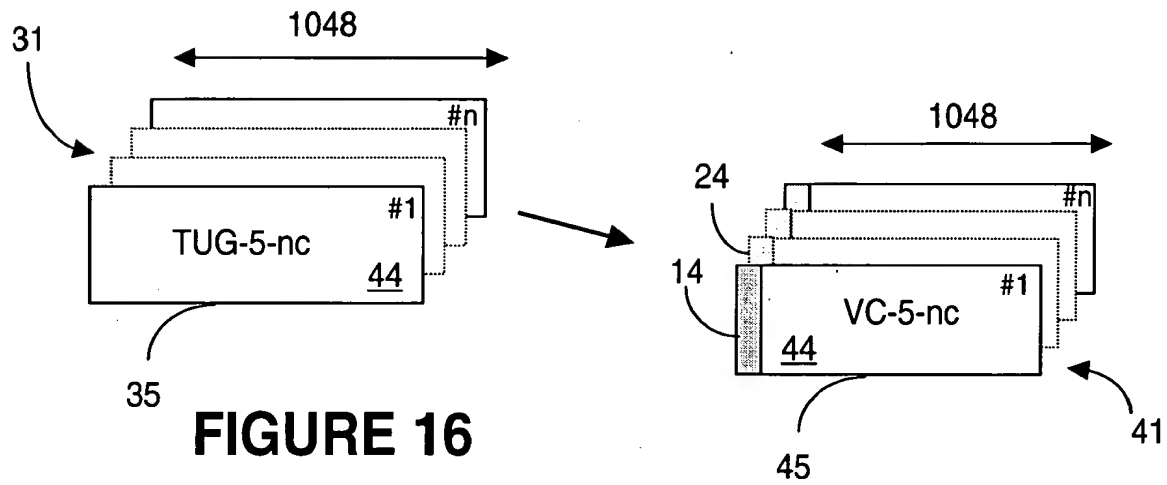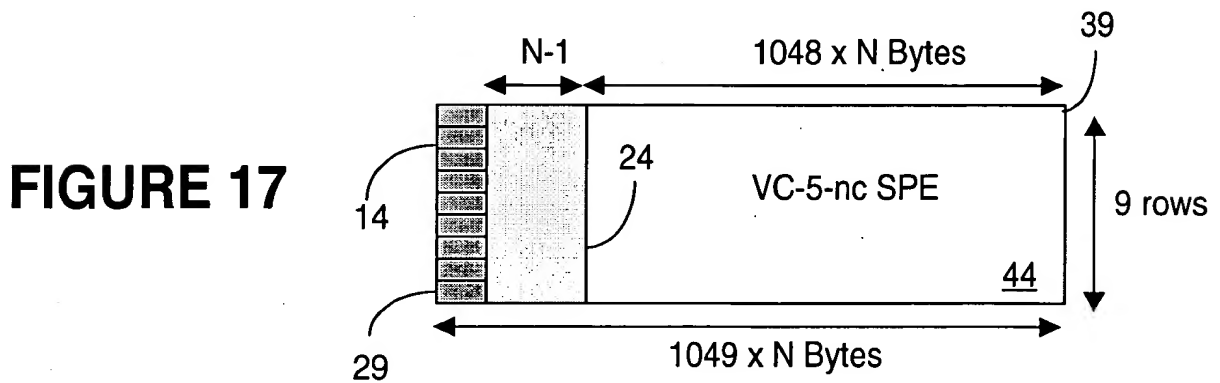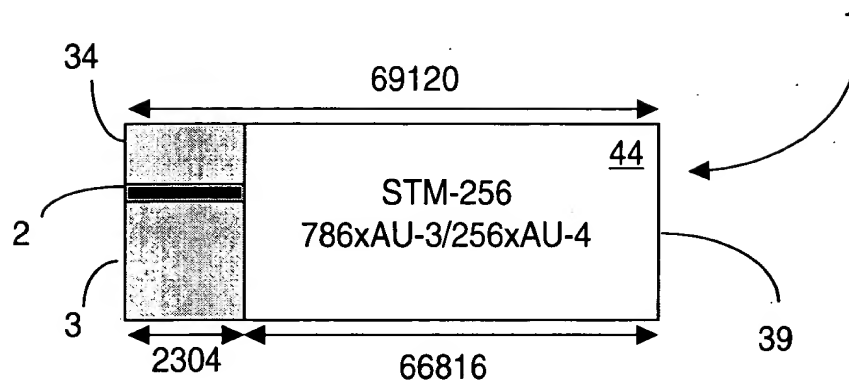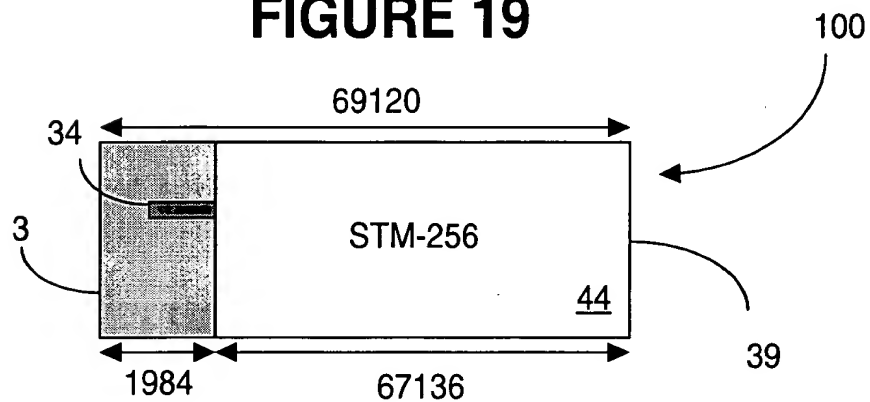
FIGURE 10

FIGURE 11

FIGURE 12

FIGURE 13

FIGURE 14

FIGURE 15

1048

#n

#1

C-5-nc

<u>44</u>

11

24

1049

#n

14

VC-5-nc

#1

<u>44</u>

21

FIGURE 16

31

1048

#n

TUG-5-nc

#1

<u>44</u>

35

24

1048

#n

14

VC-5-nc

#1

<u>44</u>

45

41

FIGURE 17

39

N-1

1048 x N Bytes

14

24

VC-5-nc SPE

9 rows

<u>44</u>

29

1049 x N Bytes

FIGURE 18

34

69120

44

STM-256
786xAU-3/256xAU-4

2

3

2304

66816

1

39

FIGURE 19

34

69120

100

3

STM-256

44

1984

67136

39

FIGURE 20

FIGURE 21